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Docket No. **26327**

Declaration and Power of Attorney For Patent Application

English Language Declaration

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole Inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

LOGIC CIRCUIT AND METHOD OF LOGIC CIRCUIT DESIGN

the s	pecification of wh	ich				
	is attached here	eto.	•			
	was filed on		_ as United Sta	tes Application No. or	r PCT	
	International App	lication Number				
	and was amender	no b				-
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knowr	owledge the duty to me to be ma n 1.56.	to disclose to the terial to patenta	United States bility as define	Patent and Tradema ed in Title 37, Code	rk Office all of federal f	information Regulations,
Sectionany P States patent	n 365(b) of any fo CT International a	reign application application which have also identif lficate or PCT Into	(s) for patent i designated a lied below: by	United States Code or inventor's certifica t least one country checking the box, an ication having a filing	other than y foreign ap	the United plication for
Prior	Foreign Application	on(s)			Priority No	ot Claimed
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60/406,751	28-August	
(Application Serial No.)	(Filing Date	2)
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(Application Serial No.)	() milg back	-,
sofar as the subject matter of each of the states or PCT International a S.C. Section 112. I acknowledge the section 1.56 which became available PCT International filing date of this	ne to be material to patentable between the filing date of the	up, as defined in Title 31, C.F.N
(Application Serlal No.)	(Filing Date)	(Status) (patented, pending, abandoned)
		(Status)
(Application Serlal No.)	(Filing Date)	(Status) (patented, pending, abandoned) (Status)

form PTO-58-01 (6-95) (Modified)

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Page 3 of 3

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